

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kimoto, Hisamitsu

Serial No.: 09/850,254

Filed: May 7, 2001

Title: Semiconductor Apparatus and  
Manufacturing Method Therefor

Attorney Docket No.: N13476100S

Group Art Unit: 2826

Examiner: Quinto, K.



RESPONSE TO OFFICE ACTION

5 Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

10 The following is submitted in response to the Office Action dated October 7, 2002, and is  
currently due January 7, 2003.

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37 C.F.R. §1.8

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Patents, Washington, D.C. 20231.

Date of Deposit: January 3, 2003

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Signature: Darryl Walker